

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

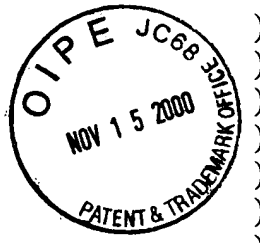
In re Application of:

Shigeru ATSUMI

Serial No. 09/028,276

Filed: February 24, 1998

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND FLASH EEPROM



Group Art Unit: 2815

Examiner: J. Fenty

Attorney Docket No. 01701.73982

#19
11-1700
Cayton

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97(i)

Assistant Commissioner of Patents
Washington, D.C. 20231

Sir:

Pursuant to their duty of good faith and candor as set forth in 37 C.F.R. § 1.56(a), Applicant submits herewith the references identified on the attached PTO Form 1449

Applicant respectfully requests that the Examiner place these references in the file in accordance with 37 C.F.R. § 1.97(i). In the event it is appropriate to consider these references, the Examiner is requested to do so and to return an initialed copy of the PTO Form 1449 to the undersigned.

Should a fee be deemed necessary, the Patent Office is authorized to charge our Deposit Account No. 19-0733.

Respectfully submitted,

Joseph M. Potenza #35509
for Joseph M. Potenza
Reg. No. 28,175

Date: November 15, 2000

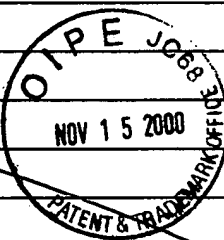
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PTO-1449 (Modified) U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT	ATTY. DOCKET NO. 01701.73982	SERIAL NUMBER 09/028,276
	APPLICANT Shigeru ATSUMI	
	FILING DATE: February 24, 1998	GROUP ART UNIT 2815

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE



FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES/NO
	63-236354	10/1988	Japan			Yes
	63-252459	10/1988	Japan			Yes
	4-365373	12/1992	Japan			Yes

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER	DATE CONSIDERED
EXAMINER: Initial citation if reference was considered. Draw line through citation if not in conformance to MPEP 609 and not considered. Include copy of this form with next communication to applicant.	

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